



BMS COLLEGE OF ENGINEERING, BENGALURU-19
Autonomous Institute, Affiliated to VTU
FACULTY PROFILE



01	Name of the Faculty		Shyamala G					
02	Date of Birth		DD	MM	YYYY	03	Sex	Female
			23	07	1978			
04	Designation		Assistant Professor					
05	Department		Computer Science and Engineering					
06	Date of Joining		22/08/2005					
07	Contact Details		Office		Mobile		Email-id	
					9964999307		Shyamala.cse@bmsce.ac.in	
08	Educational Qualifications							
	a) At the time of joining BMSCE							
Sl. No.	Degree	Subjects/Specialization	Class/Grade obtained		Year of Passing		University	
1	M-Tech	Computer Science and Engineering	First Class		2005		VTU	
2								
3								
	b) Additional Qualifications acquired during service in BMSCE							
1	Pursuing Ph D under VTU							
2								
3								
09	Positions held after joining BMSCE (Academic / Administrative / Others)							
Sl. No.	Name of the Position	No. of Years	From		To		Institution / University	
1	Assistant Professor	13	2005		2018		BMSCE	
2								
3								
4								
10	Subjects handled							
	Data Communication, Computer Networks, Computer Organization, DBMS, ADA, EC,E&M, ICL, Unix, Operating system, Cryptography and Network Security,IOT							

11	Number of candidates Awarded M.Sc., (By Research) & Ph.D. Degrees under your Guidance					
Sl. No.	Name of the Candidate	Title of the Thesis	Degree Awarded	Date of Award	University	
1						
2						
12	Number of Candidates Presently Working for M.Sc., (By Research) & Ph.D. Degree under your Guidance					
Sl. No.	Name of the Candidate	Degree for which Registered	Title of the Thesis	Commencement Year	Probable Date of Submission	
1						
2						
13	Research Publications					
	a) National Journals					
Sl. No.	Title of the Paper	Name of the Journal	Volume No.	Issue No.	Page Nos.	Year
1						
2						
3						
	b) International Journals					
Sl. No.	Title of the Paper	Name of the Journal	Volume No.	Issue No.	Page Nos.	Year
1	A Study on Obstacle Avoiding Rectilinear Steiner tree Algorithms	International Journal of Innovations in Engineering and Technology,	Volume 4	Issue 4	120-130	Dec 2014
2	A Study on Applying Parallelism for Construction of Steiner Tree Algorithms in VLSI Design	International Journal of Computer Application	Volume 148	Issue 2	7-12	August 2016
3						
4						
14	Books Published					
Sl. No.	Title of the Chapter/Book	Name of the Publisher			Year of Publication	
1						
2						
15	Details of Research Activities [Funds received for Research projects]					
	a) Funded Research Projects completed					

Sl. No.	Title of the Research Project	Funding Agency	Amount	Duration	Year (From – To)
1					
2					
b) On-going Funded Research Projects					
Sl. No.	Title of the Research Project	Funding Agency	Amount	Duration	Commencement Year
1					
2					
16	Conference/Seminars/Workshops Attended				
	a) National Conferences/Seminars/Workshops				
Sl. No.	Details of Conferences/Seminars/Workshops	Dates (From – To)	Sponsoring Agency		
1	A ONE-DAY WORKSHOP ON PARALLEL COMPUTING WITH GPUs	19/03/2016	BMSCE		
2	Workshop on Industry Academia partnership	5/1/2015	BMSCE		
3	Workshop on Automation of Curriculum Design and Attainment for OBE	6/1/2015	BMSCE		
4	Two Week ISTE STTP on Introduction to Design of Algorithms Conducted by IIT-Kharagpur National Mission on Education through ICT (MHRD, Govt. of India)	25/May/2015 to 30/May/2015	BMSCE		
5	International Advance Computing Conference	12/06/2015 to 13/06/2015	BMSCE		
6	Research Methodologies and latex conducted by VTU e-learning centre at ghousiacollege,Ramanagaram.	27/7/2013 to 29/7/2013	BMSCE		
7	Faculty development program on Advanced Software Engineering Concept at TCS Bangalore	13/3/2014 to 14/3/2014	BMSCE		
8	Workshop on Discovery services and JGate	15/3/2014	BMSCE		
9	Parallel Computing architecture and applications on multicore to manycore processing systems organised by IIT Bombay	16/6/2014 to 20/6/2014	BMSCE		
10	Think Parallel : Parallel programming for Engineers and scientists organized by C-DAC Bangalore	23/6/2014 to 27/6/2014	BMSCE		
11	Workshop on “Trends and Challenges of Android Computing”	30/10/2013 to 31/10/2013	BMSCE		
12	15 days workshop on “Software Testing”	19 th -1 st September 2006	BMSCE		
13	Faculty development program “Enabling	On 05-02-2008 to	BMSCE		

	the Guru”	06-02-2008 at L&T organized by L&T infotech		
14	Training programme for Symbian OS:Essentials & system programming	2 nd -9 th December 2008 at Symbian India.	BMSCE	
15	Faculty development program on “Modern teaching methods”	BMSCE, 20/07/2009 to 24/07/2009	BMSCE	
16	Workshop on “Software tools, techniques and practices for Engineers and Scientists” [STTPES]	10 th -17 th June 2011 conducted by Dept of ISE, BMSCE in association with Fossee project, IIT Bombay.	BMSCE	
17	Two days Faculty development programme on “Multi Core Programming”	28 th - 29 th March 2011 at PESIT, Bangalore	BMSCE	
18	“Publishing and Reviewing Papers in Engineering Education” IUCEE.	07/06/2016-26/07/2016	BMSCE	
19	“Access to e-resources for teaching and research” Train the Trainer program, VTU Consortium	4/5/2017-5/5/2017	BMSCE	
20	Building Scalable CISCO Internetworks	4th January 2018 to 9 th January 2018	Tequip-III	
b) International Conferences/Seminars/Workshops				
1	International Conference on Data Engineering and Communication Systems	30 th -31 st December 2011, at Vivanta Taj	BMSCE	
2	International Conference On Recent Trends In Electronics Information Communication Technology, India	May 19-20, 2017	BMSCE	
3				
Sl. No.	Details of Conferences/Seminars/Workshops	Dates (From – To)	Sponsoring Agency	
17	Conference/Seminars/Workshops Papers Presented			
a) National Conferences/Seminars/Workshops				
Sl. No.	Details of Conferences/Seminars/Workshops	Presented Papers (Title of the Paper Presented)	Dates (From – To)	Sponsoring Agency

1				
2				
b) International Conferences/Seminars/Workshops				
Sl. No.	Details of Conferences/Seminars/Workshops	Presented Papers (Title of the Paper Presented)	Dates (From – To)	Sponsoring Agency
1	IEEE International Conference On Recent Trends In Electronics Information Communication Technology, India	Presented paper on Obstacle Aware Delay Optimized Rectilinear Steiner Minimum Tree Routing	May 19-20, 2017,	BMSCE
2	Inventive Communication and Computational Technologies (ICICCT), 2017 International Conference	Exploring the parallel implementations of the three classical MST algorithms	March 10-11,2017	BMSCE
18	Conference/Seminars/Workshops Organized			
a) National Conferences/Seminars/Workshops				
Sl. No.	Details of Conferences/Seminars/Workshops	No of Participants	Dates (From – To)	Sponsoring Agency
1	Workshop on Artificial Intelligence	107	3/2/2017-4/2/2017	BMSCE
2	Workshop on Bigdata Analytics	50	1day 28/2/2017	BMSCE
3	Android App development workshop by wingfotech in association with IIT Madras	60	04/03/2016 to 05/03/2016	BMSCE
4	One day workshop on “Research trends in image processing and computer vision”	25	18/03/2016	BMSCE
5	Workshop on “Linux and python programming”	30	19/05/16 to 20/05/16	BMSCE
6	Workshop on “Linux Systems and applications	40	09/11/2017 to 14/11/2017	BMSCE
b) International Conferences/Seminars/Workshops				
Sl. No.	Details of Conferences/Seminars/Workshops	No of Participants	Dates (From – To)	Sponsoring Agency
19	Refresher Courses/Orientation Programs/Training Programs Attended			
Sl. No.	Details of Refresher Courses/Orientation Programs/Training Programs	Attended as Resource Person/Participant	Duration (From – To)	Sponsoring Agency
1	“Modern teaching methods”	Participant	20/07/2009 to	BMSCE

			24/07/2009		
20	Refresher Courses/Orientation Programs/Training Programs Organized				
Sl. No.	Details of Refresher Courses/Orientation Programs/Training Programs	No of Participants	Duration (From – To)	Sponsoring Agency	
21	Assignments/Deputations to Outside Institutions/Universities				
Sl. No.	Nature of Assignment	Institution/University where Assignment taken	Duration (From – To)	Sponsoring Agency	
22	Honors/Awards/Distinction Received				
Sl. No.	Details of Honors/Awards/Distinction Received	Conferring Agency	Date of Award		
23	Membership in Professional Bodies/University Bodies/Organizations				
Sl. No.	Type of Membership	Professional Bodies/Organizations/ University Bodies/Others	Period of Membership		
1		ISTE			
24	Patents Registered				
Sl. No.	Name of the Invention	Date of Application	Application Number	Date of Patent Awarded	Patent Number

Date:

Name & Signature of the Faculty